

Patentee:	Vogelsang	Docket No.:	QIM 2002 P 12738 US
Serial No.:	10/720,437	Patent No.:	7,293,190 B2
Filed:	November 24, 2003	Issue Date:	November 6, 2007
For:	Noisy Clock Test Method and Apparatus		

**SLATER & MATSIL, L.L.P.**  
17950 Preston Rd.  
Suite 1000  
Dallas, Texas 75252  
Tel.: 972-732-1001  
Fax: 972-732-9218

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,293,190 B2  
APPLICATION NO. : 10/720,437  
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INVENTOR(S) : Vogelsang

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Col. 9, line 8, delete "clack" and insert --clock--.  
In Col. 9, line 12, delete "clack" and insert --clock--.  
In Col. 10, line 18, delete "elect-rically" and insert --electrically--.

### MAILING ADDRESS OF SENDER

Slater & Matsil, L.L.P.  
17950 Preston Rd.  
Suite 1000  
Dallas, TX 75252

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